

110

参考文献図 下部基板

REFERENCE GRADATION  
VOLTAGE GENERATOR 120

基準階調  
電圧発生部

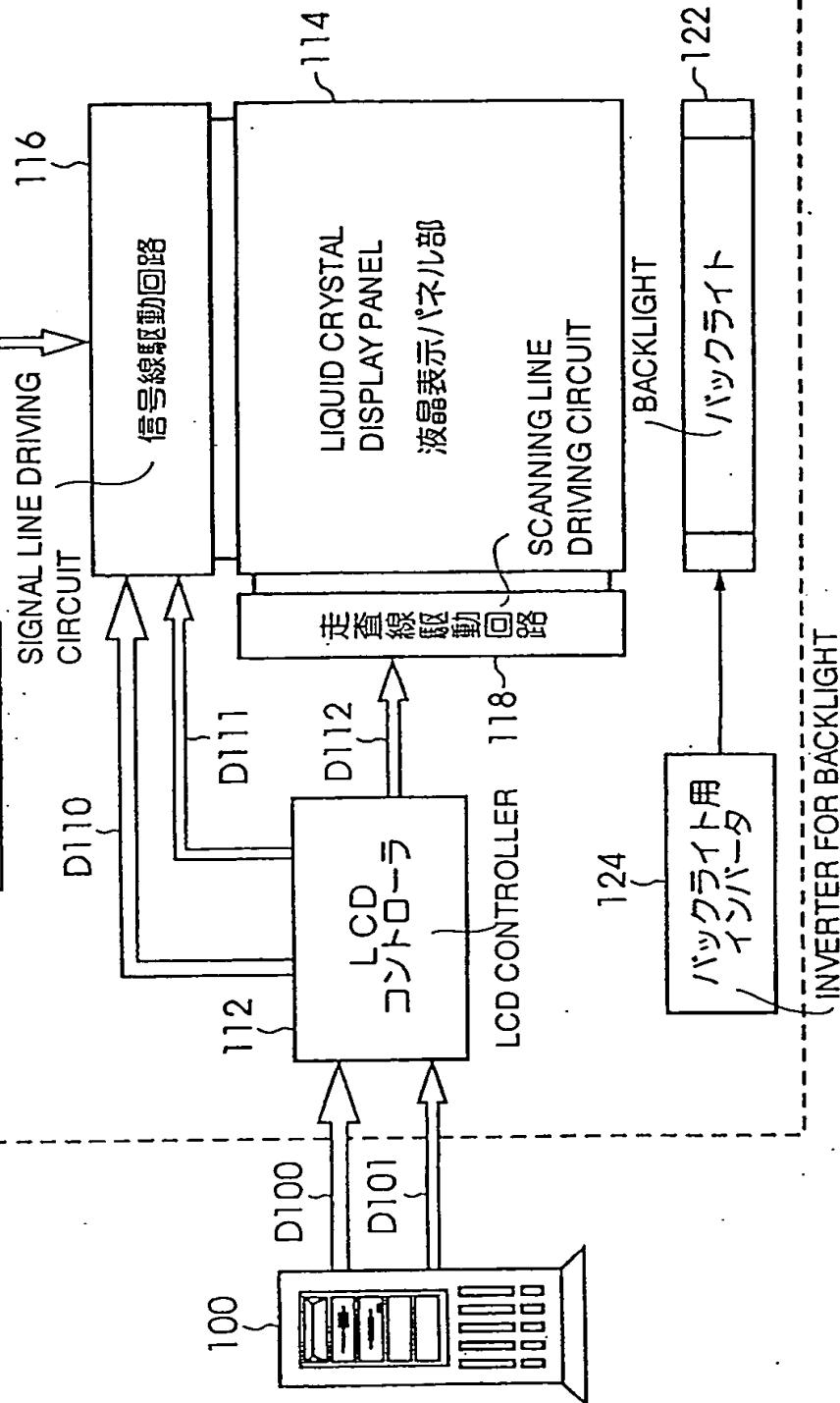
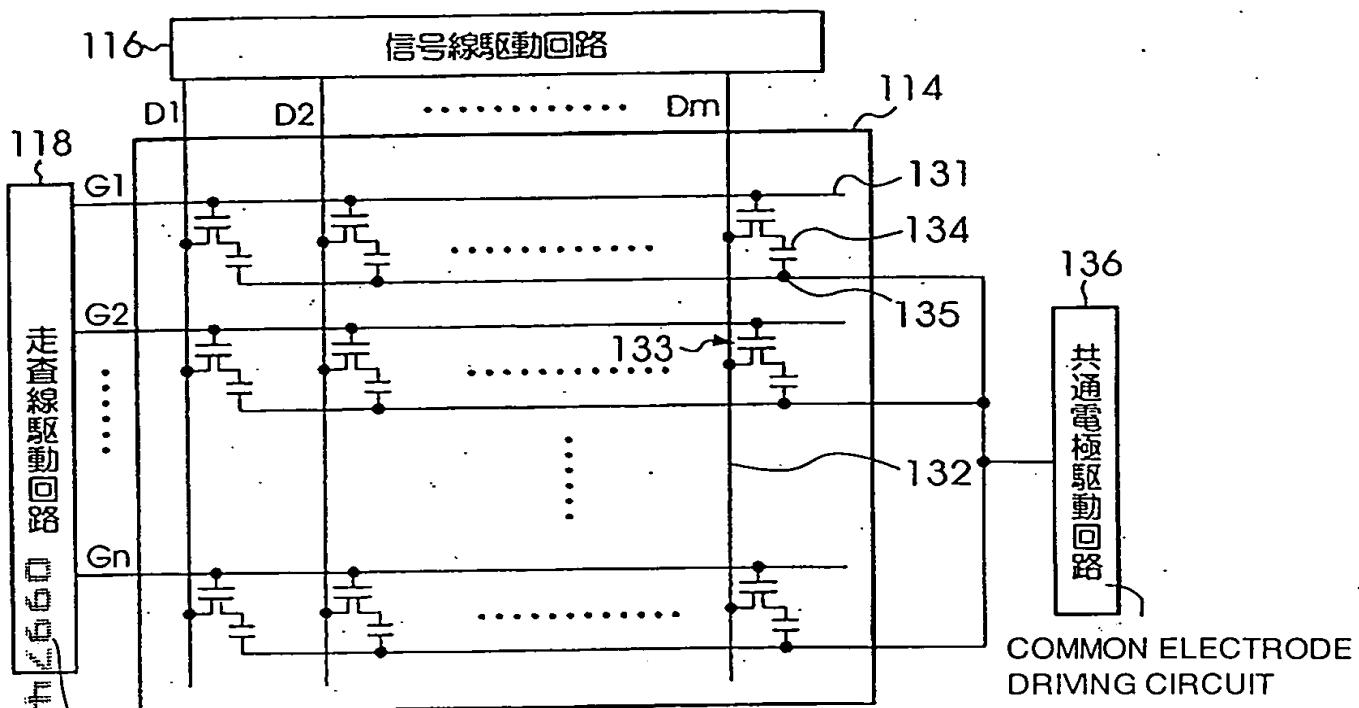


FIG. 1

SIGNAL LINE DRIVING CIRCUIT



SCANNING LINE  
DRIVING CIRCUIT

FIG. 2

COMMON ELECTRODE  
DRIVING CIRCUIT

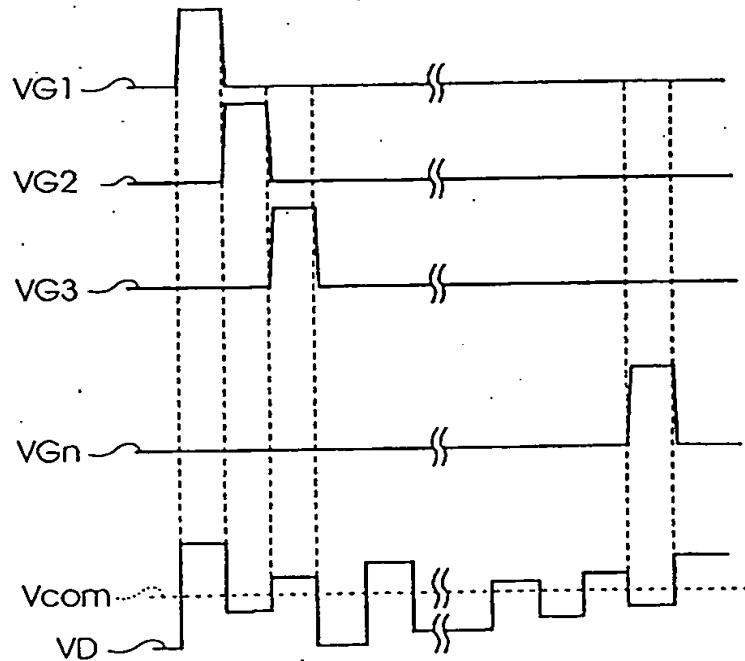


FIG. 3

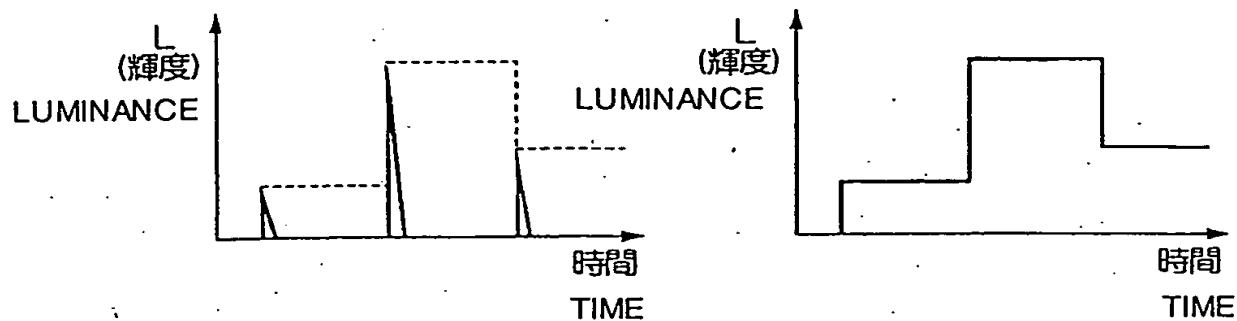


FIG. 4A

FIG. 4B

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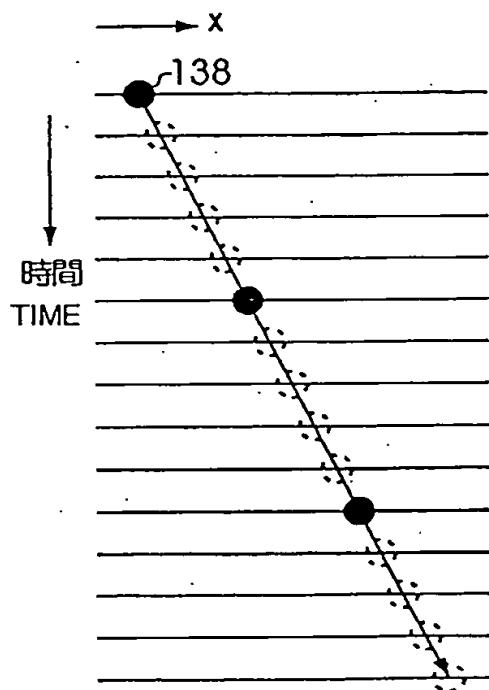


FIG. 5A

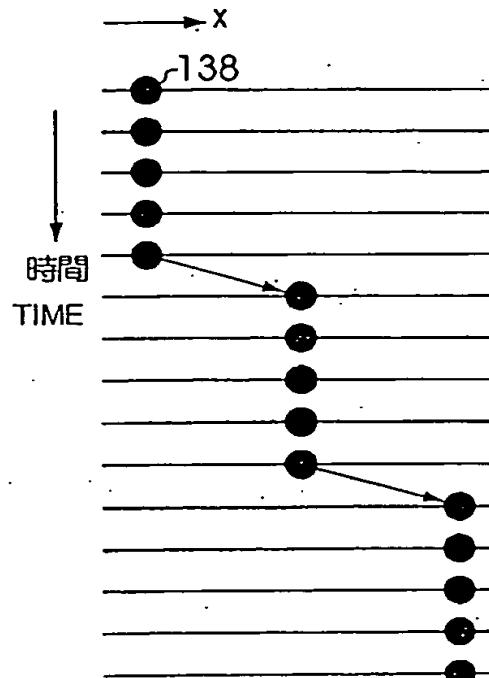


FIG. 5B

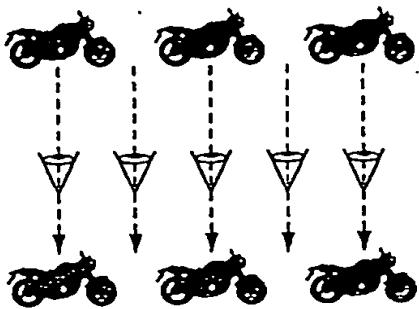


FIG. 6A

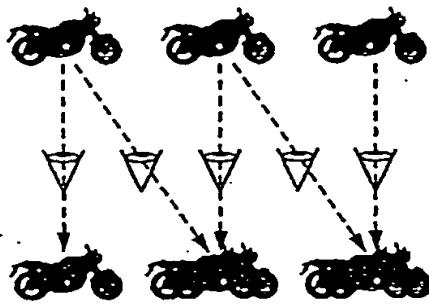


FIG. 6B

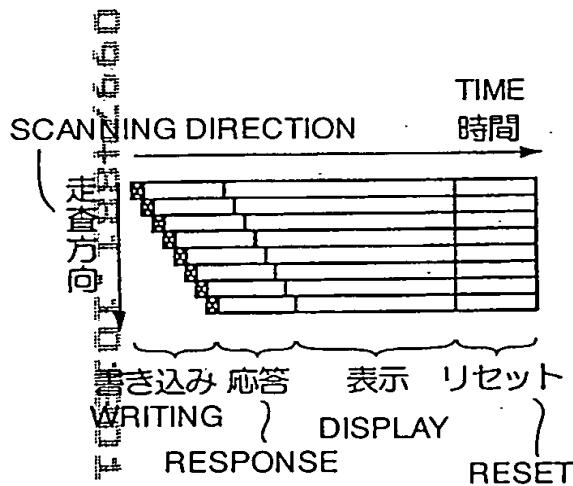


FIG. 7A

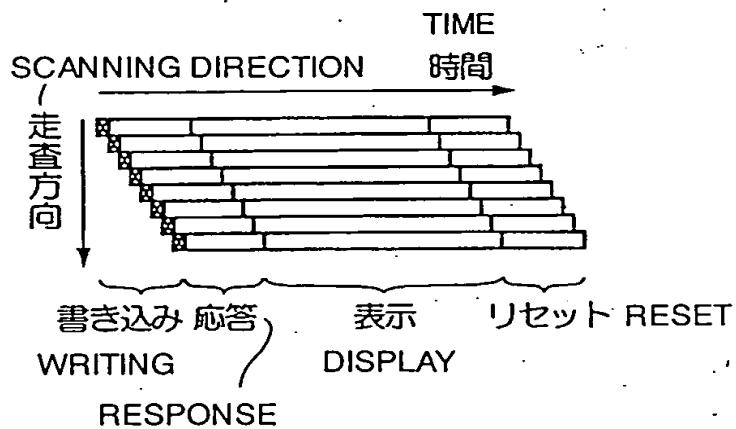


FIG. 7B

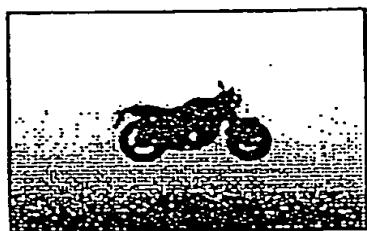


FIG. 7C

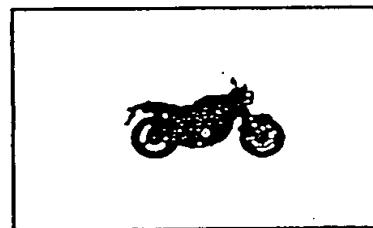


FIG. 7D

SIGNAL LINE DRIVING  
CIRCUIT

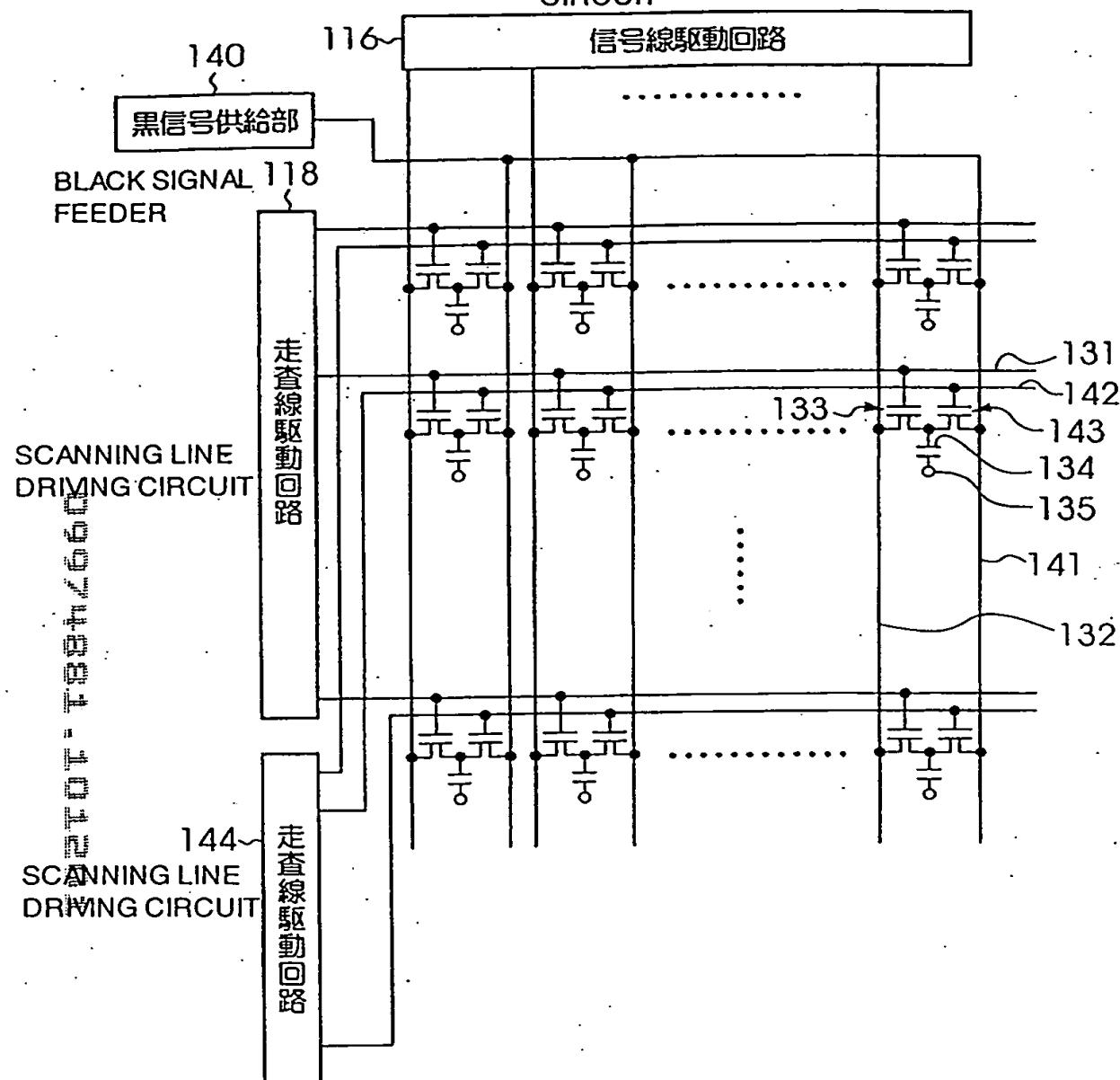


FIG. 8  
PRIOR ART

REFERENCE GRADATION  
VOLTAGE GENERATOR リファレンス電圧発生部

40

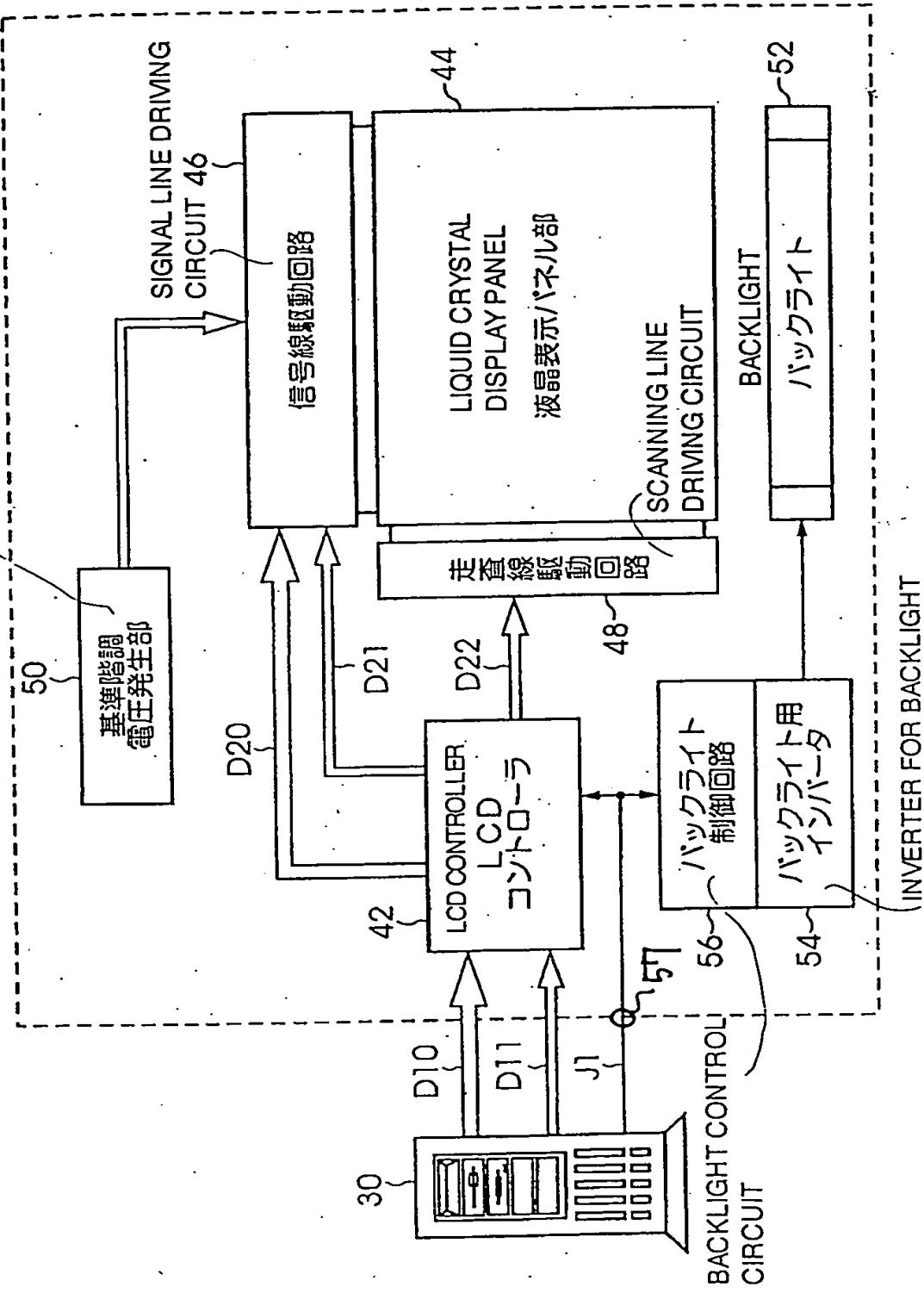


FIG. 9

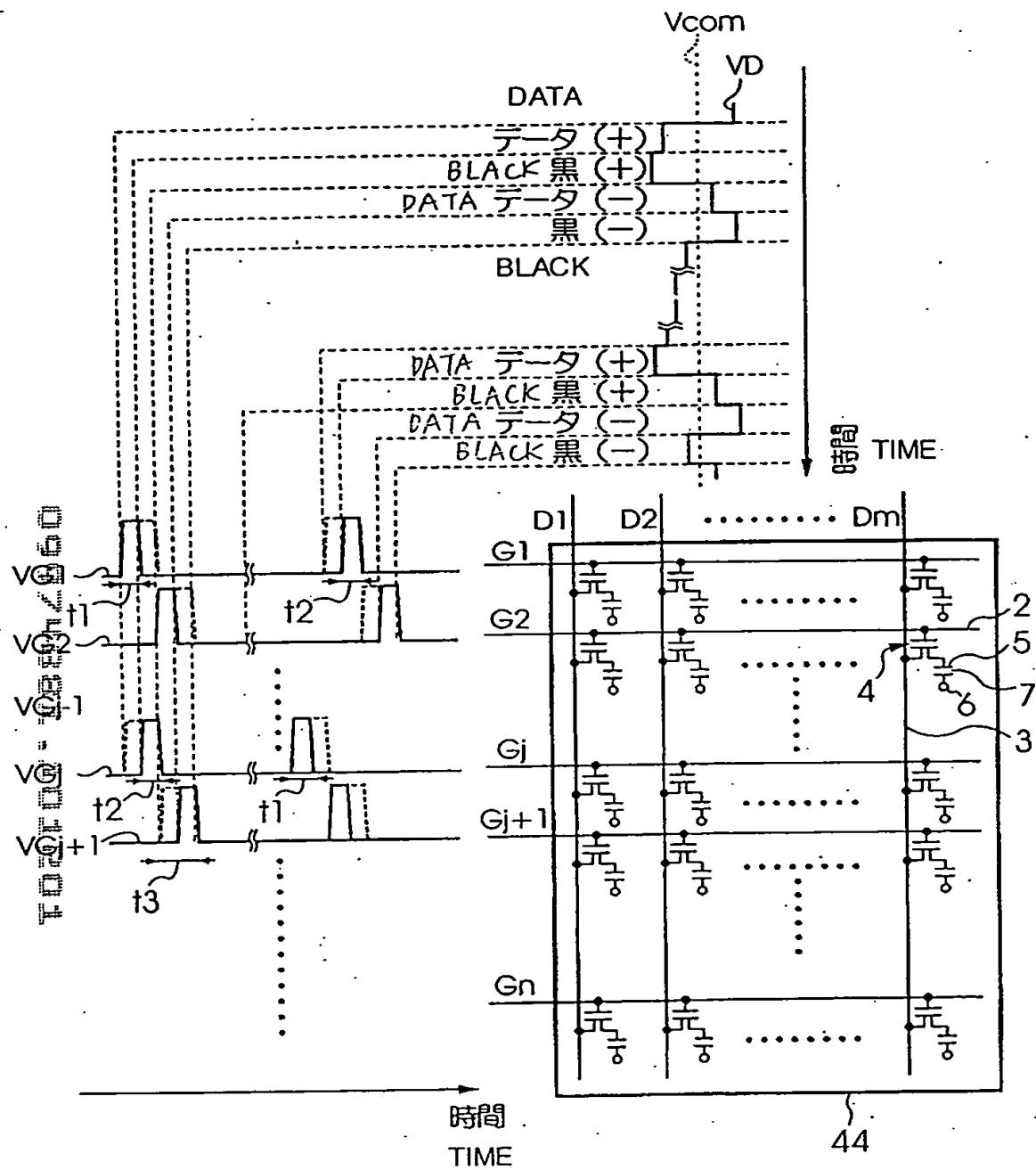


FIG. 10

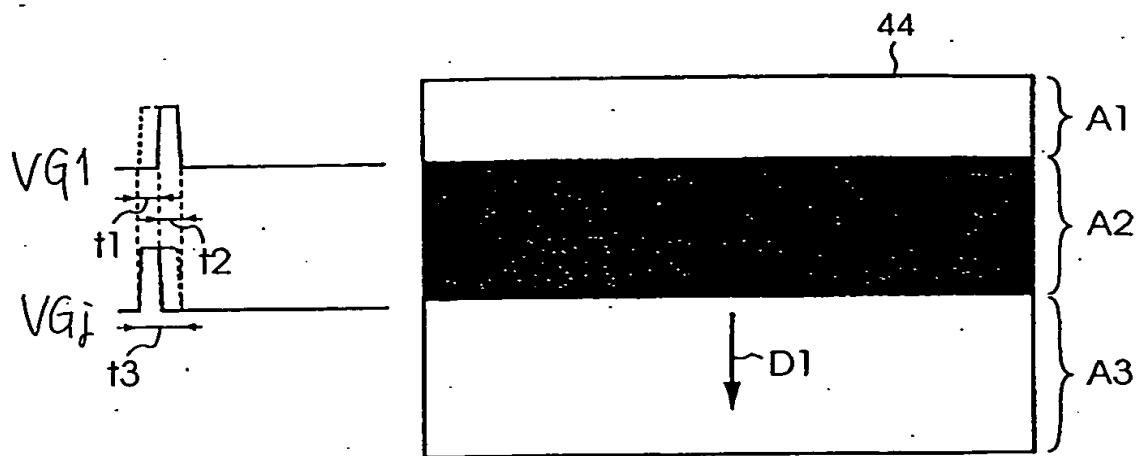


FIG. 11

OPIUS - TECNOLOGY

REFERENCE GRADATION  
VOLTAGE

D20 Data

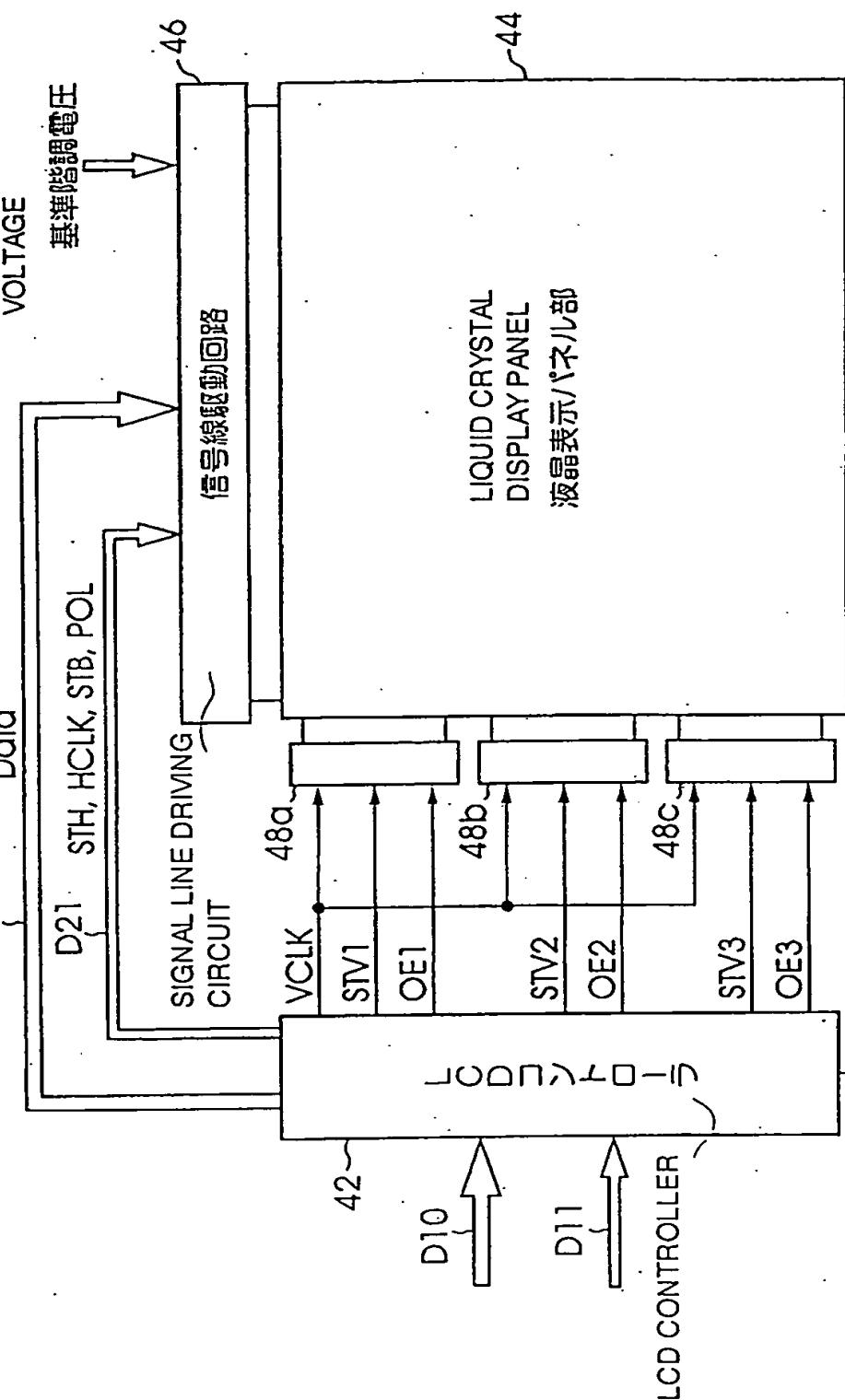


FIG. 12

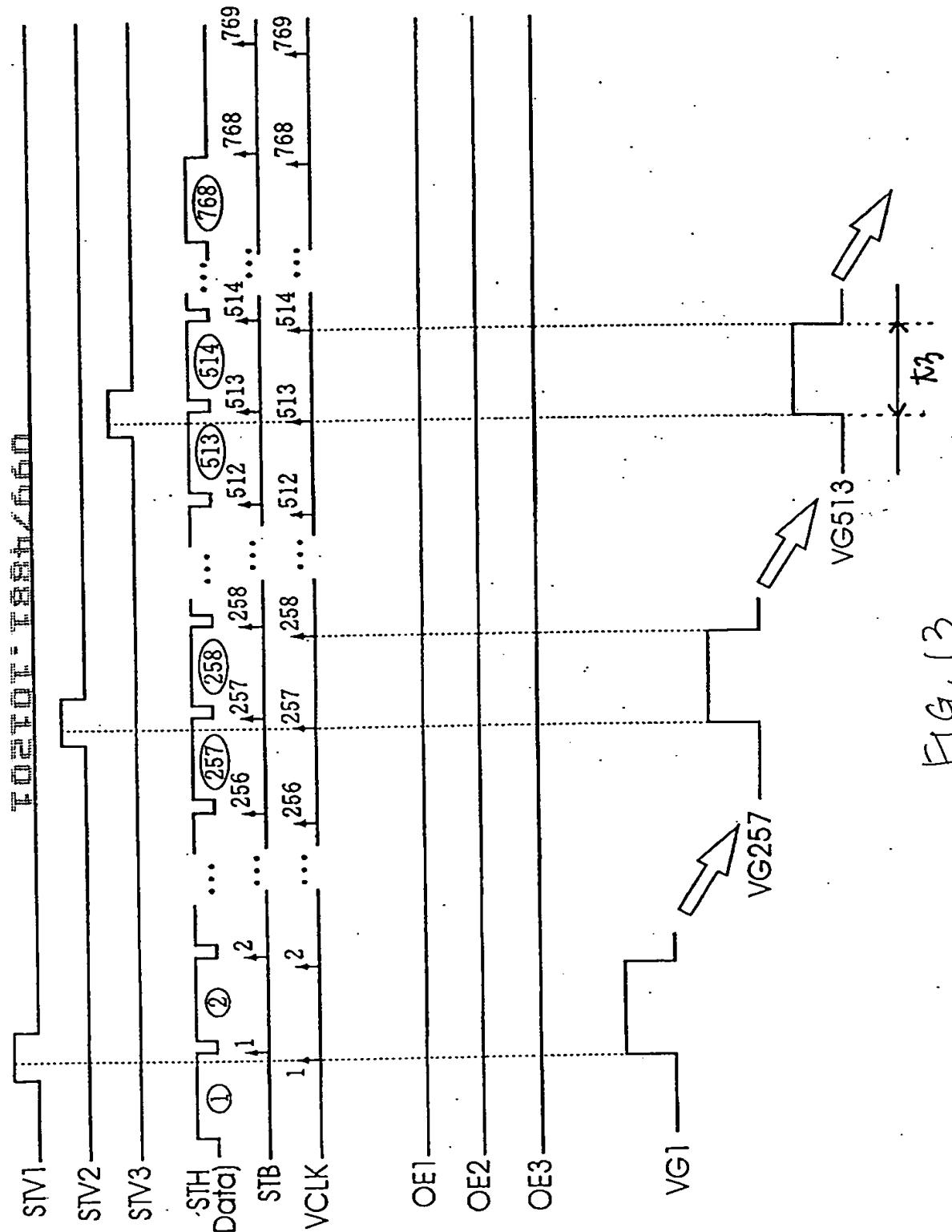


FIG. 13

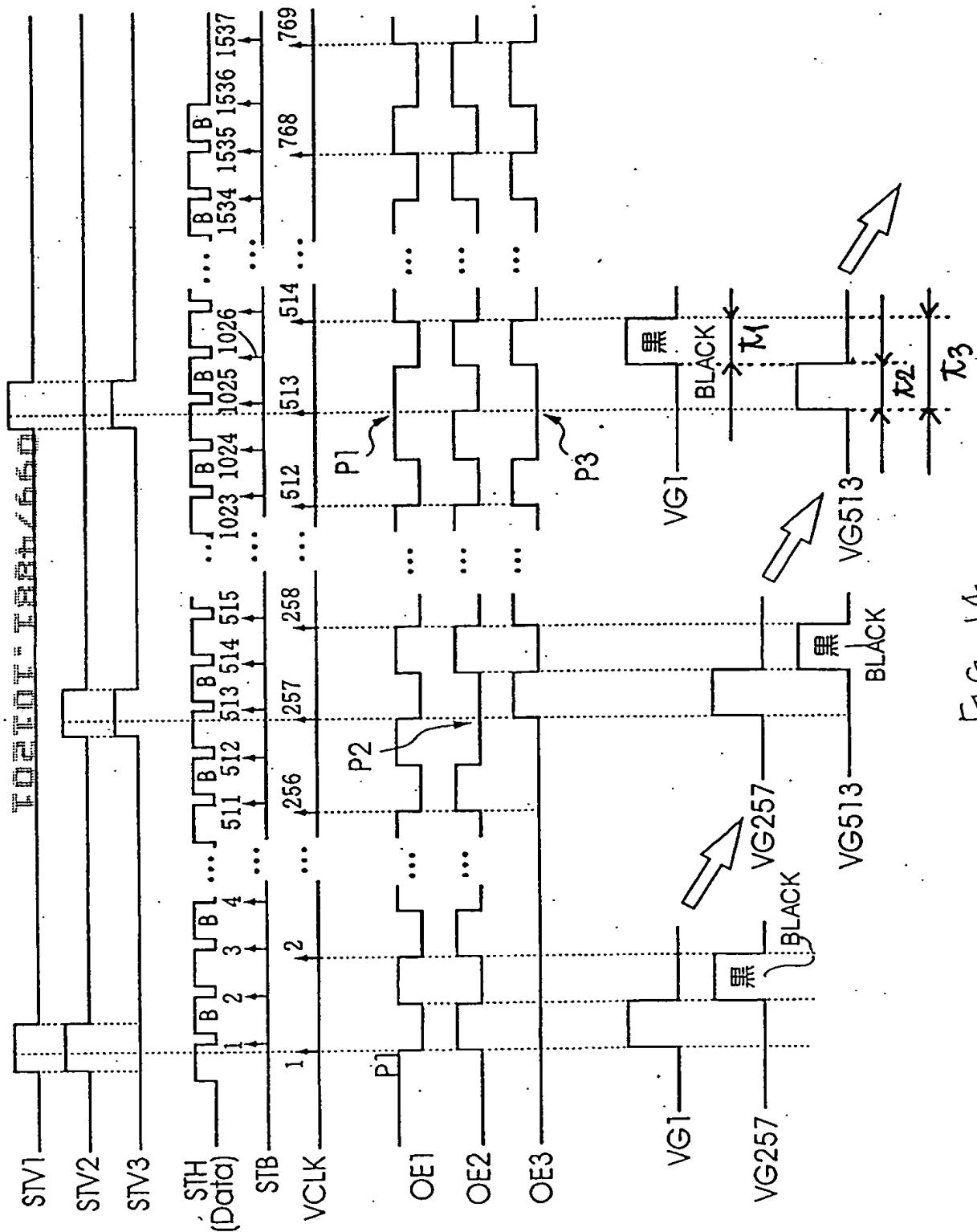


FIG. 14

0002410  
0002410  
0002410  
0002410  
0002410

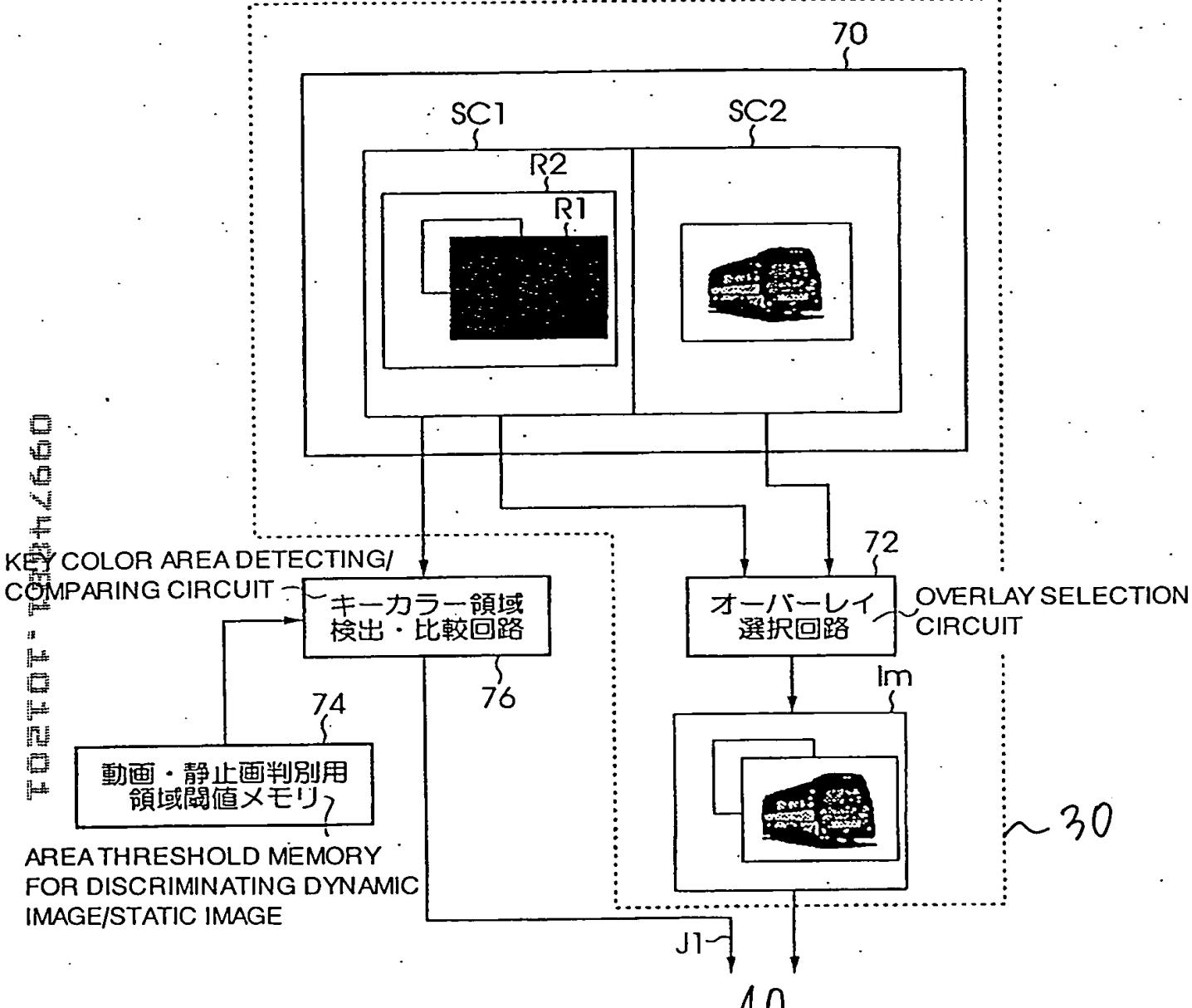
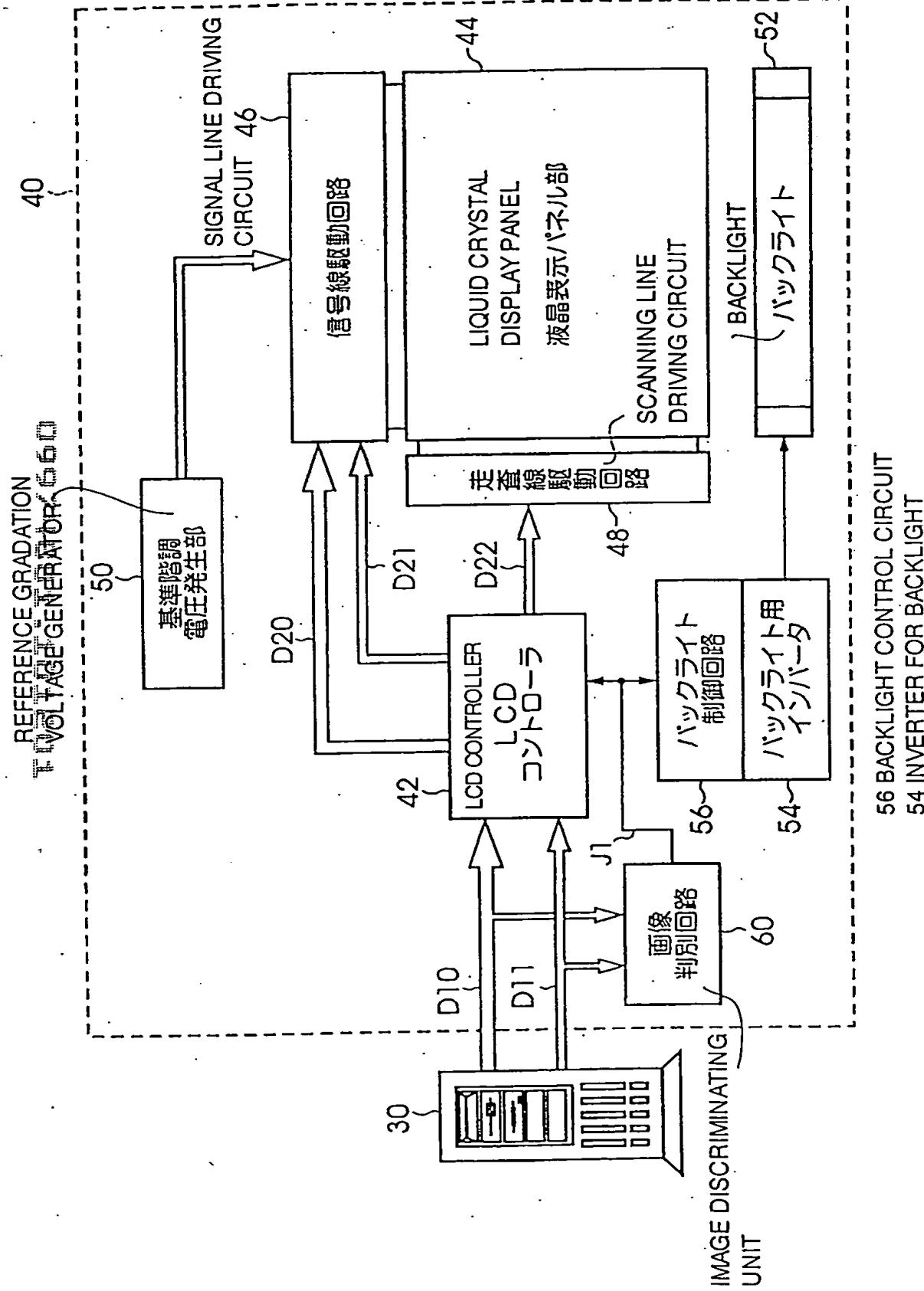


FIG. 15



56 BACKLIGHT CONTROL CIRCUIT  
54 INVERTER FOR BACKLIGHT

FIG. 16.

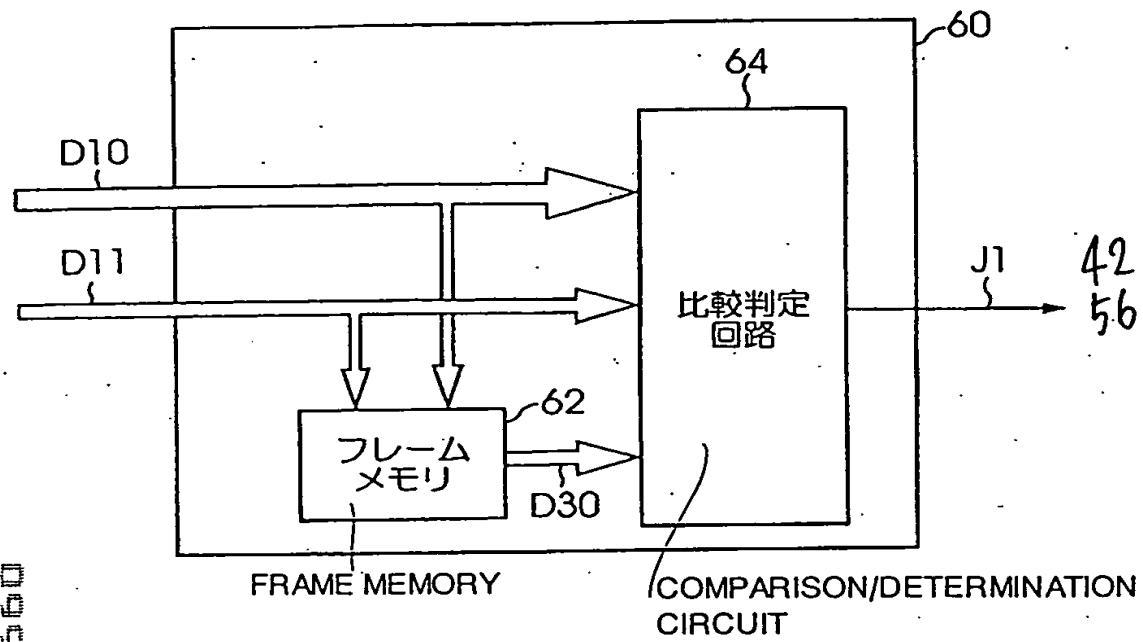


FIG. 17

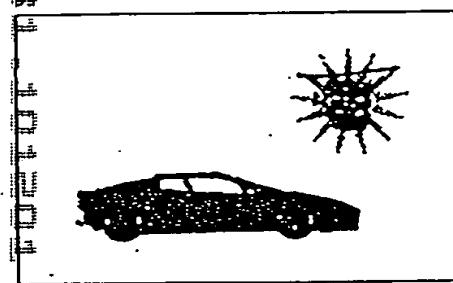


FIG. 18A

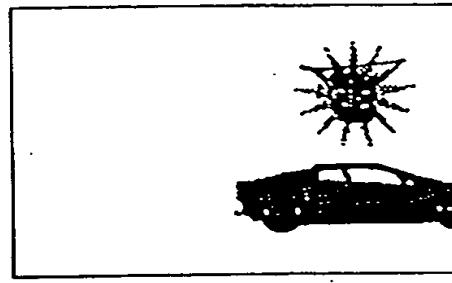


FIG. 18B